

Refine Search

Search Results -

Terms	Documents
L2 and ((without or "not") adj3 (signal\$3 or inform\$3))	8

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Set Name **Query**
side by side

Hit Count **Set Name**
result set

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L3 L2 and ((without or "not") adj3 (signal\$3 or inform\$3)) 8 L3

L2 L1 same (queu\$3 or buffer\$3) 204 L2

L1 (transaction or task or job) near3 (reorder\$3 or (re adj1 order\$3)) 624 L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
(709/100 709/208 710/110 710/107 710/263 710/41 710/52 710/311 718/100 718/101 718/102 718/103 718/104 718/105 718/106 718/107 718/108 711/151 714/47).ccls.	13445

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Search: L4

Search History

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<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u>
side by side			result set
<u>DB</u> =PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; <u>PLUR</u> =YES; <u>OP</u> =OR			
<u>L4</u>	710/110,107,263,41,52,311;709/100,208;714/47;711/151;718/100-108.ccls.	13445	<u>L4</u>
<u>L3</u>	L2 and ((without or "not") adj3 (signal\$3 or inform\$3))	8	<u>L3</u>
<u>L2</u>	L1 same (queu\$3 or buffer\$3)	204	<u>L2</u>
<u>L1</u>	(transaction or task or job) near3,(reorder\$3 or (re adj1 order\$3))	624	<u>L1</u>

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L2 and L4	54

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<u>Set</u>	<u>Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set</u>
				<u>Name</u> result set
side by side				
DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR				
<u>L5</u>	L2 and L4		54	<u>L5</u>
<u>L4</u>	710/110,107,263,41,52,311;709/100,208;714/47;711/151;718/100-108.ccls.		13445	<u>L4</u>
<u>L3</u>	L2 and ((without or "not") adj3 (signal\$3 or inform\$3))		8	<u>L3</u>
<u>L2</u>	L1 same (queu\$3 or buffer\$3)		204	<u>L2</u>
<u>L1</u>	(transaction or task or job) near3 (reorder\$3 or (re adj1 order\$3))		624	<u>L1</u>

END OF SEARCH HISTORY

EAST - [Untitled1:1]

File View Edit Tools Window Help



- Drafts
- Pending
- Active
 - L1: (339) (transaction or ta
 - L2: (123) 11 same (queu\$3 or
 - L3: (45) 12 same (slave or d
- Failed
- Saved
- Favorites
- Tagged (0)
- UDC
- Queue
- Trash

Search List Browse Queue Clear

DBs USPAT

Plurals

Highlight all hit terms initially

Default operator: OR

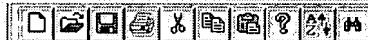
BRS form IS&R form Image Text HTML

Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Err
1	BRS	L1	339 (transaction or task or job) near3 (reorder\$3 or	USPAT	2007/07/23 13:00			
2	BRS	L2	123 11 same (queu\$3 or buffer\$3)	USPAT	2007/07/23 13:01			
3	BRS	L3	45 12 same (slave or device or (I adj1 O) or (input	USPAT	2007/07/23 13:03			

Start > EAST - [Un...]

EAST - [Untitled1:1]

File View Edit Tools Window Help



- Drafts
- Pending
- Active
 - L1: (339) (transaction or ta
 - L2: (123) 11 same (queu\$3 or
 - L3: (45) 12 same (slave or d
- Failed
- Saved
- Favorites
- Tagged (0)
- UDC
- Queue
- Trash

Search List Browse Queue Clear

DBs USPAT

Plurals

Default operator: OR

Highlight all hit terms initially

12 same (slave or device or (I adj1 O) or (input adj1 output))

BRS form IS&R form Image Text HTML

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
1	<input type="checkbox"/>	<input type="checkbox"/>	US 7181556 B2	20070220	17	Transaction request servicing mechanism	710/110	710/389; 709/230;
2	<input type="checkbox"/>	<input type="checkbox"/>	US 7162546 B2	20070109	5	Reordering unrelated transactions from an ordered	710/5	710/33; 710/36;
3	<input type="checkbox"/>	<input type="checkbox"/>	US 7139859 B2	20061121	10	Inter-queue ordering mechanism	710/306	710/311; 710/315
4	<input type="checkbox"/>	<input type="checkbox"/>	US 7103684 B2	20060905	18	Single-chip USB controller reading power-on boot code	710/62	710/20; 710/22;
5	<input type="checkbox"/>	<input type="checkbox"/>	US 7099986 B2	20060829	127	High speed peripheral interconnect apparatus,	710/314	710/306; 710/311

Start | > | EAST - [Un...]

EAST - [Untitled1:1]

File View Edit Tools Window Help



- Drafts
- Pending
- Active
 - L1: (339) (transaction or ta
 - L2: (123) 11 same (queu\$3 or
 - L3: (45) 12 same (slave or d
- Failed
- Saved
- Favorites
- Tagged (0)
- UDC
- Queue
- Trash

Search List Browse Queue Clear

DBs USPAT

Plurals

Default operator: OR

Highlight all hit terms initially

12 same (slave or device or (I adj1 O) or (input adj1 output))

BRS form IS&R form Image Text HTML

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
12	<input type="checkbox"/>	<input type="checkbox"/>	US 6883045 B1	20050419	26	Apparatus for reordering graphics responses in a	710/52	710/20; 710/21;
13	<input type="checkbox"/>	<input type="checkbox"/>	US 6862647 B1	20050301	15	System and method for analyzing bus transactions	710/313	710/305; 710/306;
14	<input type="checkbox"/>	<input type="checkbox"/>	US 6857033 B1	20050215	16	I/O node for a computer system including an	710/62	710/2; 710/305;
15	<input type="checkbox"/>	<input type="checkbox"/>	US 6839784 B1	20050104	15	Control unit of an I/O node for a computer system	710/240	710/112; 710/113;
16	<input type="checkbox"/>	<input type="checkbox"/>	US 6834319 B1	20041221	15	Tunnel device for an input/output node of a	710/307	710/29; 710/313;

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Efficient job scheduling in a mesh multicomputer without discrimination against large jobs

Dugki Min Mutka, M.W.
Dept. of Comput. Sci., Kon-Kuk Univ., Seoul, South Korea;
This paper appears in: [Parallel and Distributed Processing, 1995. Proceedings, Seventh IEEE Symposium on](#)
Publication Date: 25-28 Oct. 1995
On page(s): 52 - 59
Meeting Date: 10/25/1995 - 10/28/1995
Location: San Antonio, TX
INSPEC Accession Number:5119665
Digital Object Identifier: 10.1109/SPDP.1995.530664
Posted online: 2002-08-06 20:08:33.0

Abstract

Many innovative schemes for allocating jobs to parallel computing systems have been proposed in order to achieve highly utilized parallel computing systems. The schemes have tried to achieve good job response times with little system fragmentation of processing resources. Since most schemes have concentrated on approaches for processor allocation, the schemes have used First-Come-First-Serve (FCFS) as the job scheduling discipline. However, it has been previously established that job scheduling algorithms for parallel computing systems can have a large impact on the system utilization and job response time. Schemes that use multiple queues, which reorder the sequence of jobs allocated to the parallel system, can be very effective in improving the system performance. However, such non-FCFS schemes have been criticized because they provide improved average performance by favoring small jobs at the expense of large jobs. In order to achieve improved performance by means of multiple queue job scheduling schemes without sacrificing the fairness of FCFS, we propose a new job scheduling discipline that behaves in a FCFS manner under low loaded conditions, but exploits performance enhancing features of multiple queue schemes under highly loaded conditions. In addition, the scheme does not inappropriately discriminate against large jobs

Index Terms

Inspec

Controlled Indexing
multiprocessing systems performance evaluation processor scheduling resource allocation scheduling

Non-controlled Indexing

First-Come-First-Serve discrimination job allocation job response time job response
times job scheduling job scheduling algorithms large jobs mesh multicomputer multiple
queue job scheduling schemes multiple queues parallel computing systems processor
allocation small jobs system fragmentation system performance system utilization

Author Keywords
Not Available

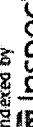
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